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IN THE U.S. PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of:

APPLICANTS: Antti Ruha et al.

SERIAL NO.: 10/005,766

FILING DATE: 11/02/2001

EXAMINER: Pablo N. Tran

ART UNIT: 2618

ATTORNEY'S DOCKET NO.: 872.0100.U1(US)

TITLE: MULTI-MODE I/O CIRCUITRY SUPPORTING LOW INTERFERENCE  
SIGNALING SCHEMES FOR HIGH SPEED DIGITAL INTERFACES

Mail Stop Appeal Brief - Patents  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO OFFICE ACTION**

Sir:

This paper is herewith filed in response to the Examiner's Office Action mailed on May 4, 2009 for the above-captioned U.S. Patent Application. As this response is herewith filed within the shortened statutory period no petition for an extension of time or late fee is believed to be due. However, should the undersigned agent be mistaken, please consider this a petition for any extension of time that may be required to maintain the pendency of this Patent Application, and charge deposit account no. 50-1924 for any required fee deficiency.

Please amend the application as shown below.